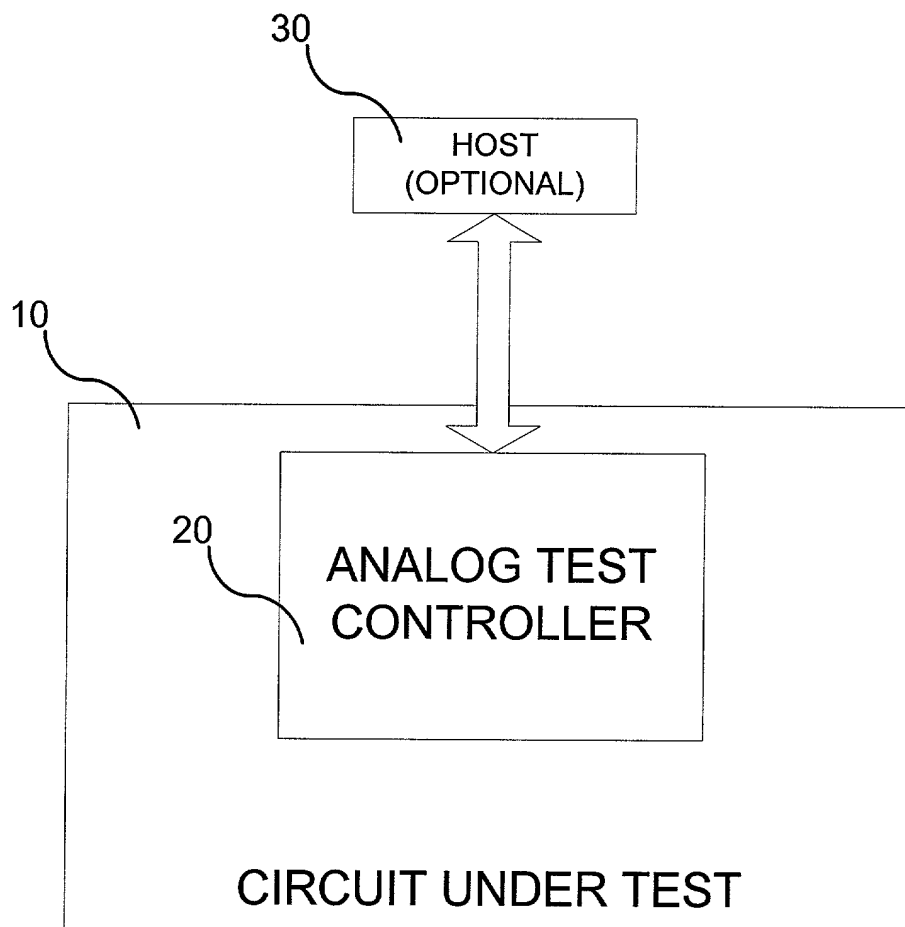


FIG. 1



1039440301 1039440301

FIG. 2

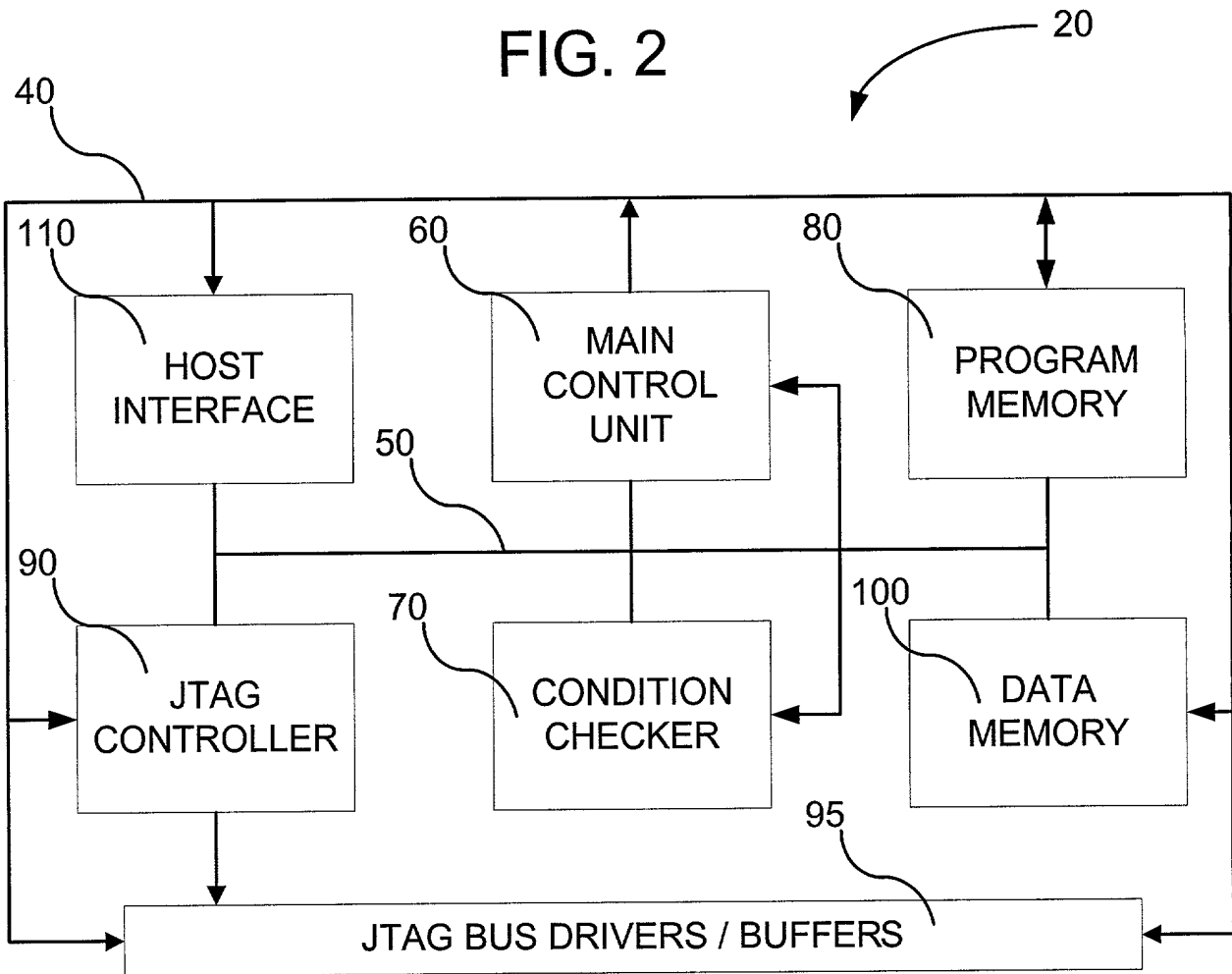
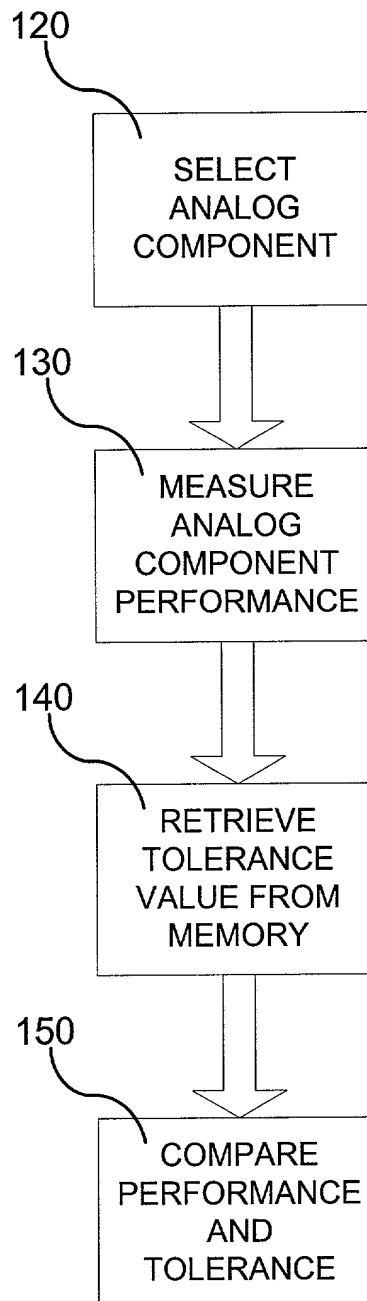
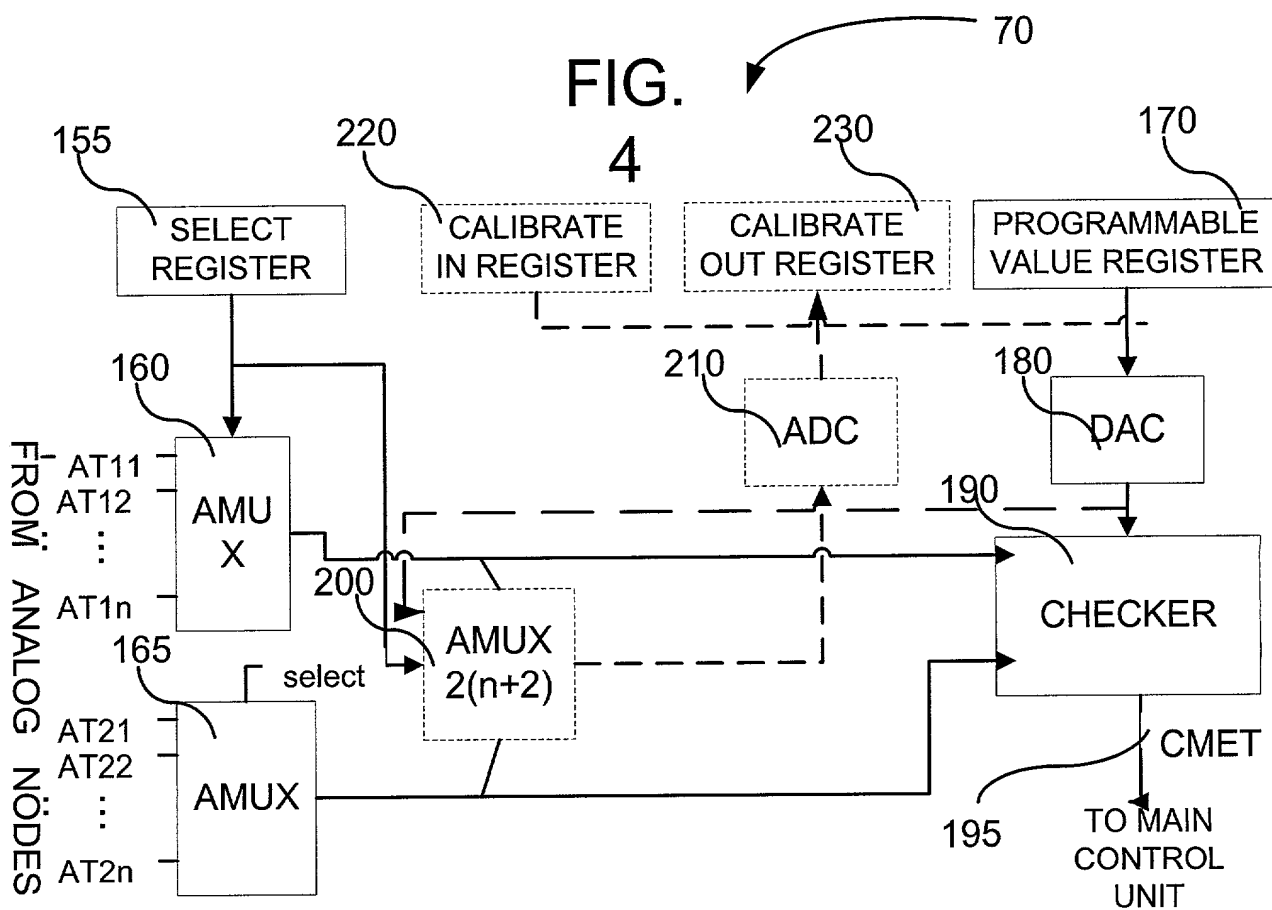


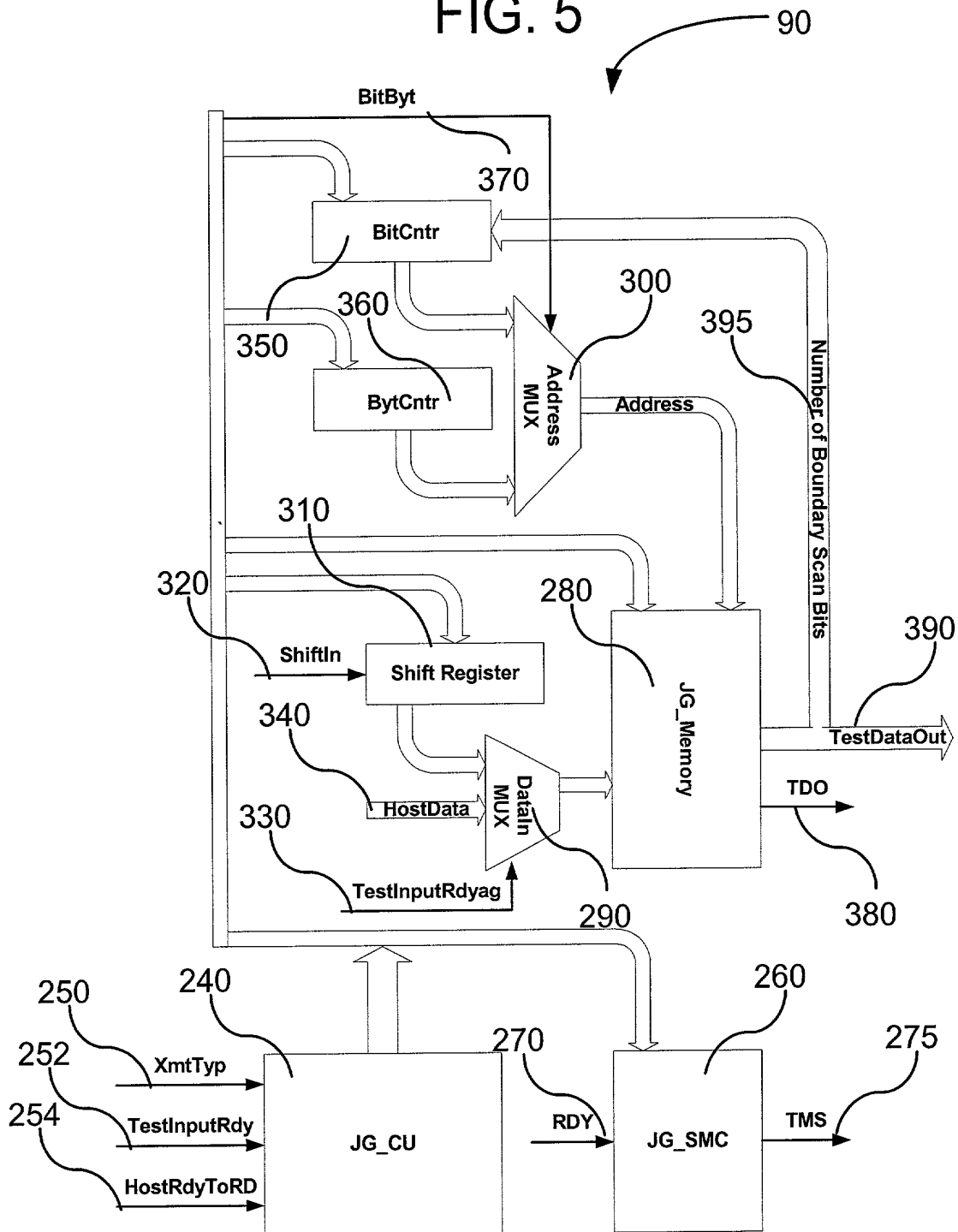
FIG. 3





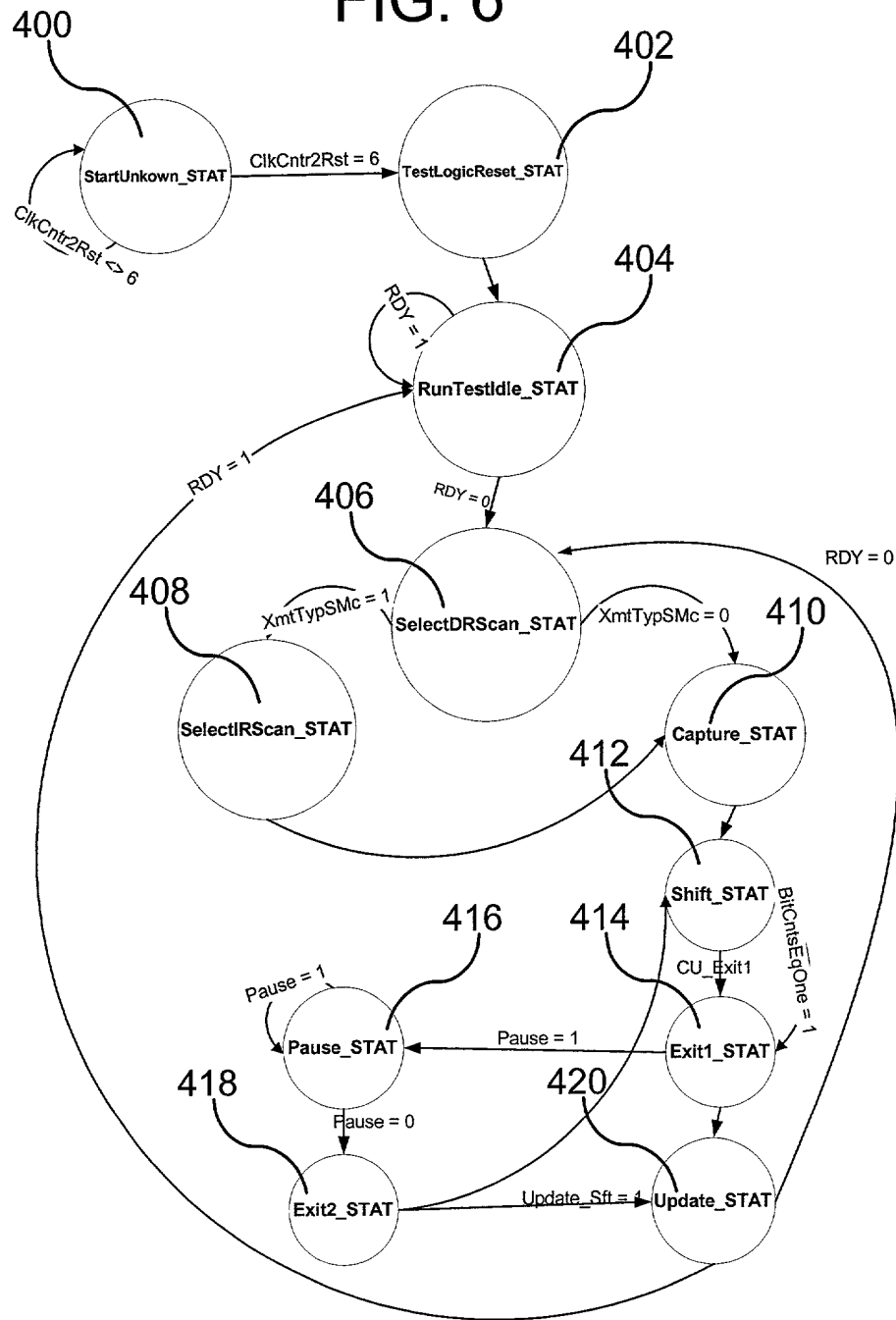
10039934-100001

FIG. 5



10039934-100601

FIG. 6



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FIG. 6a

STATE	CONTROL							OUTPUT
	R D Y	X m t T y p S M c	C U - E X I T 1	B i t C n t s E q O n e	P a u s e	U p d a t e S f t	C l k C n t r 2 R s t	TMS
StartUnknown_STAT	X	X	X	X	X	X	X	1
TestLogicReset_STAT	X	X	X	X	X	X	X	0
RunTestIdle_STAT	1	X	X	X	X	X	X	0
RunTestIdle_STAT	0	X	X	X	X	X	X	1
SelectDRScan_STAT	X	1	X	X	X	X	X	1
SelectDRScan_STAT	X	0	X	X	X	X	X	0
SelectIRScan_STAT	X	X	X	X	X	X	X	0
Capture_STAT	X	X	X	X	X	X	X	0
Shift_STAT	X	X	1	X	X	X	X	1
Shift_STAT	X	X	X	1	X	X	X	1
Shift_STAT	X	X	0	0	X	X	X	0
Exit1_STAT	X	X	X	X	0	X	X	1
Exit1_STAT	X	X	X	X	1	X	X	0
Pause_STAT	X	X	X	X	0	X	X	1
Pause_STAT	X	X	X	X	1	X	X	0
Exit2_STAT	X	X	X	X	X	1	X	1
Exit2_STAT	X	X	X	X	X	0	X	0
Update_STAT	0	X	X	X	X	X	X	1
Update_STAT	1	X	X	X	X	X	X	0

FIG. 6a

FIG. 7

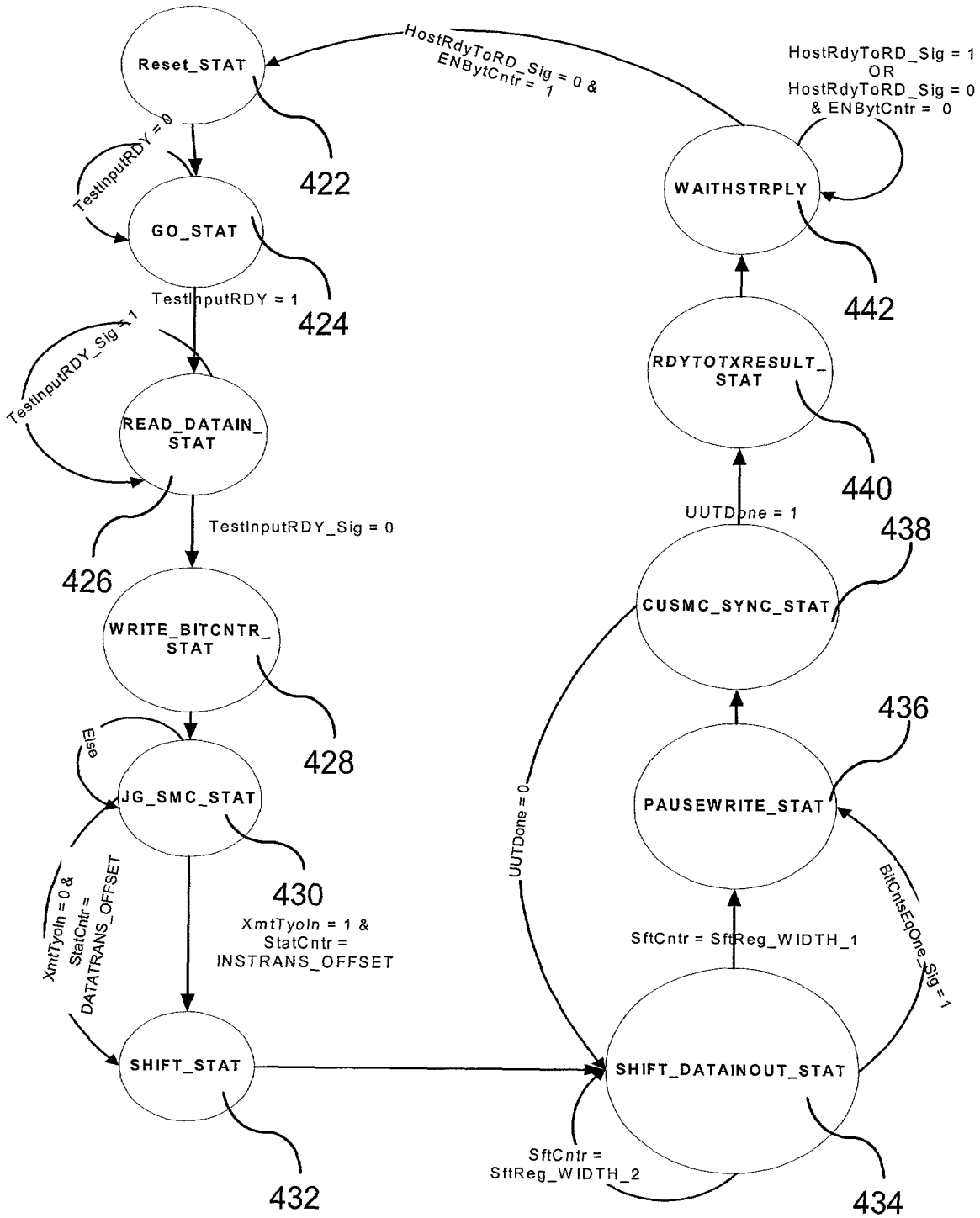
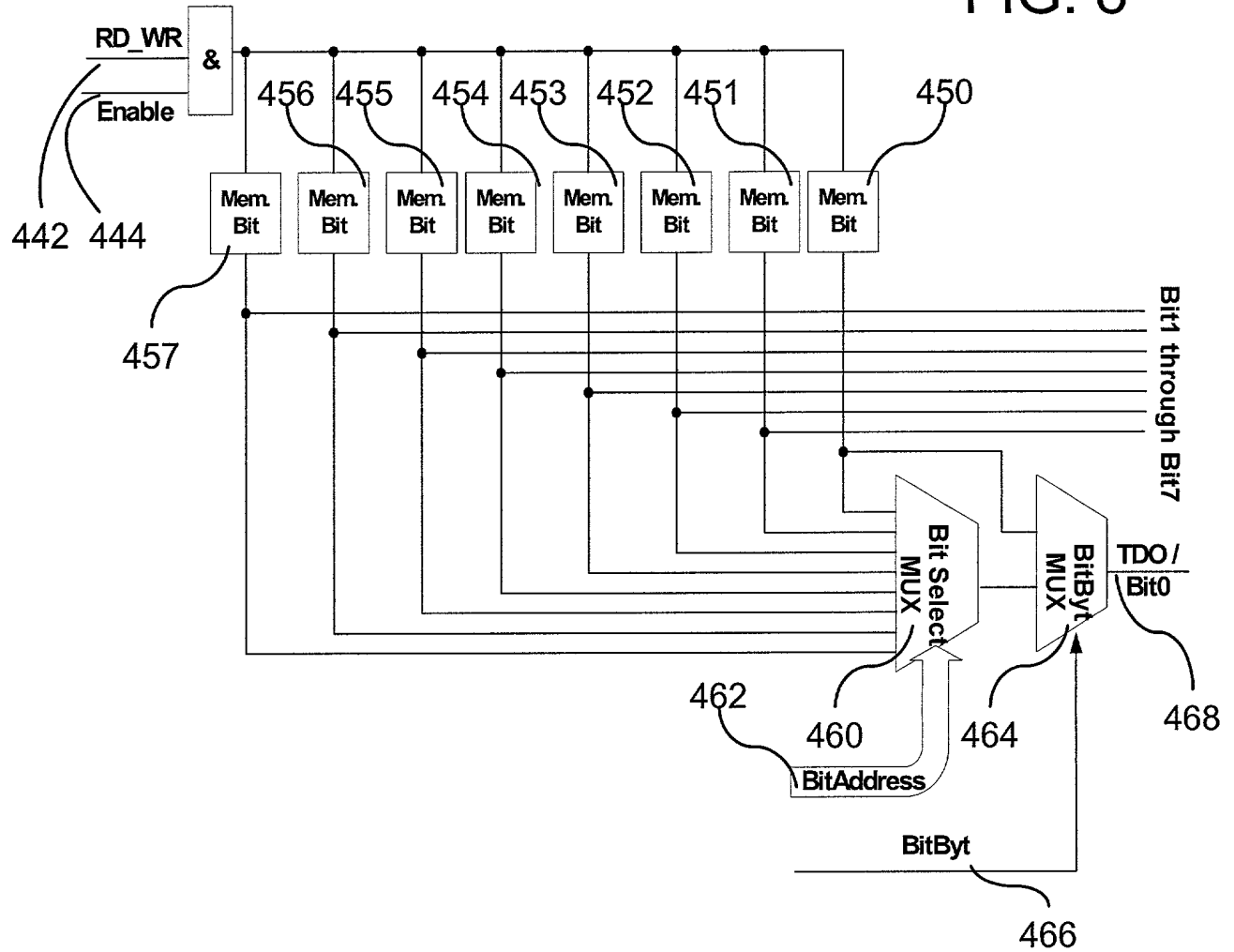


FIG. 7

FIG. 7A

[illegible]

FIG. 8



10039934-100601

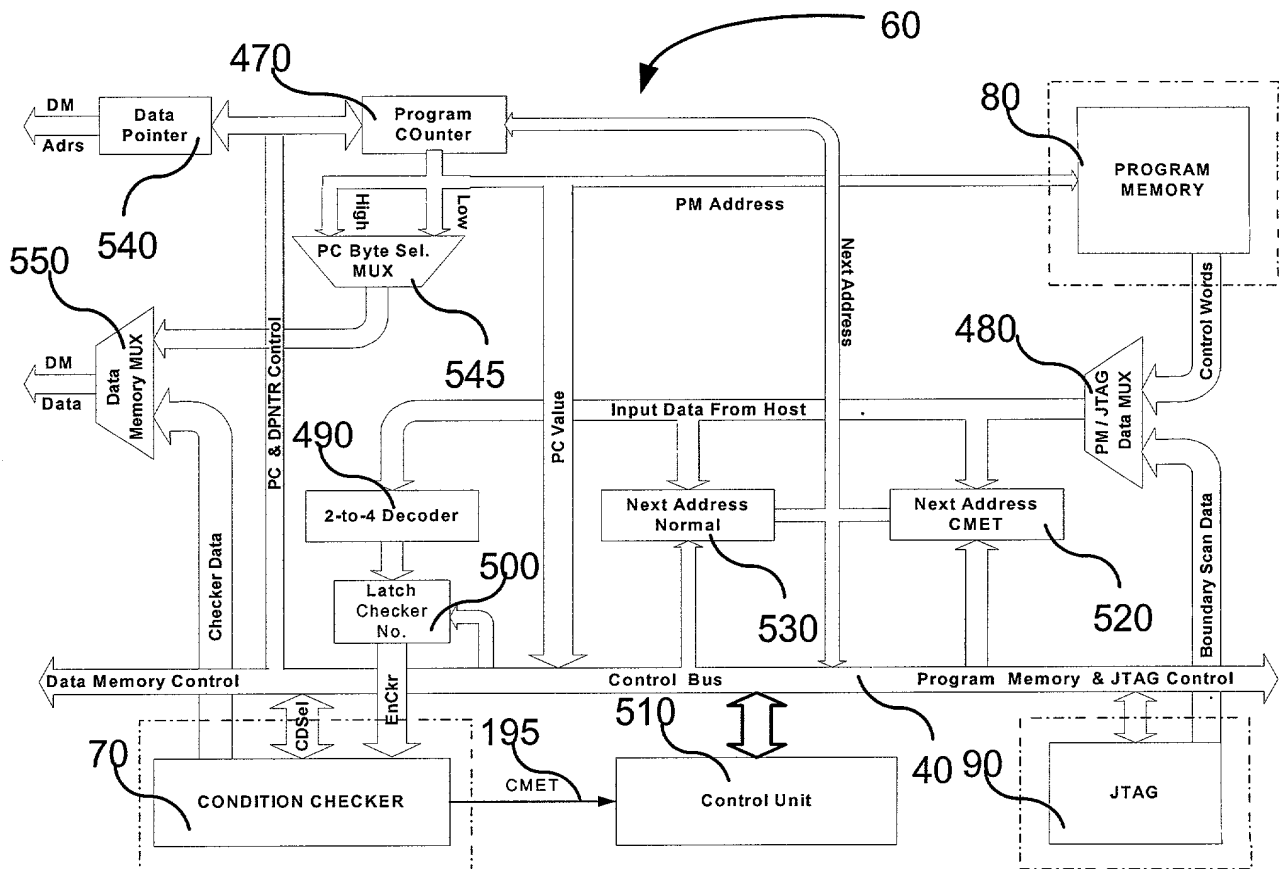


FIG. 10

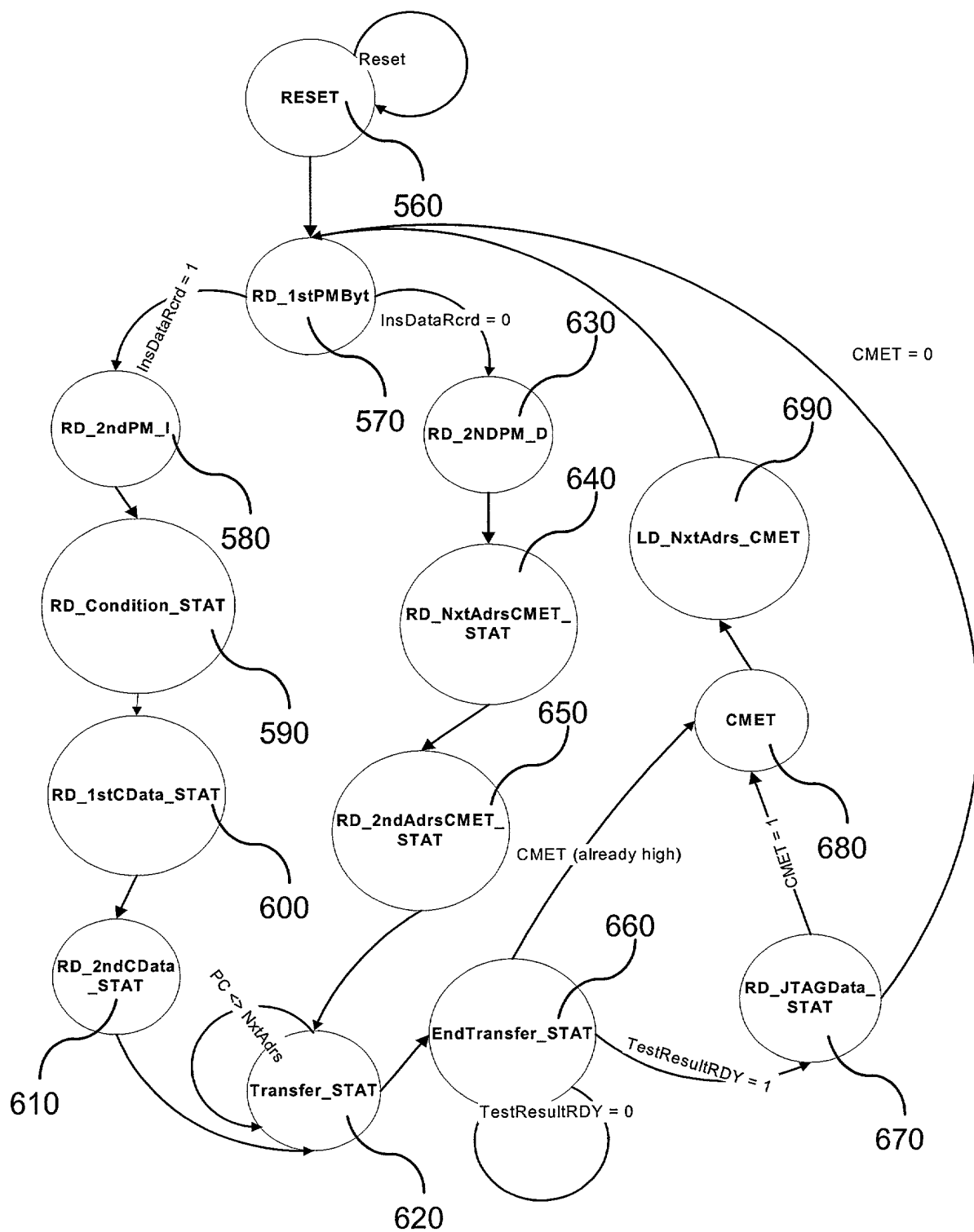


FIG. 10

STAT / CONTROL OUTPUT

STAT / CONTROL OUTPUT	C M E T	N x t A d r s N r m i _ L R e g + 1 > = P C	T e s t I n p u t R D Y	X m t T Y P	D M _ O E _ S i g	D M _ W E _ S i g	D M _ C E _ S i g	P M _ O E _ S i g	P M _ W E _ S i g	P M _ C E _ S i g	P J G D M u x _ S i g	C h k r P C _ D a t a M u x _ S i g	S R e s e t _ C h k r _ D O	C D S e e l e c t _ S i g	X m t T Y P	T e s t I n p u t R D Y _ S i g	T e s t I n p u t R D Y a g _ S i g	H o s t R d y T o R D _ S i g	S R e s e t _ D P n t r	E n d P n t r
RESET STAT	X	X	X	X	1	0	0	1	1	1	1	0	1	"00"		0	0	0	1	0
RD_1stPMemByt	1	X	X	X	1	0	0	0	1	0	1	0	1	"00"		0	0	0	0	0
RD_1stPMemByt	0	X	X	X	1	0	0	0	1	0	1	0	1	"00"		0	0	0	0	1
RD_2ndPMemByt_I	X	X	X	X	1	1	1	0	1	0	1	1	1	"00"		0	0	0	0	0
RD_2ndPMemByt_D	X	X	X	X	1	1	1	0	1	0	1	1	1	"00"		0	0	0	0	0
RD_Condition_STAT	X	X	X	X	1	1	1	0	1	0	1	1	0	CONDITION_TYP		0	0	0	0	0
RD_1stCData_STAT	X	X	X	X	1	1	1	0	1	1	1	1	0	CONDITION_1stData		1	1	0	0	0
RD_2ndCData_STAT	X	X	X	X	1	1	1	0	1	0	1	1	0	CONDITION_2ndData		1	1	0	0	0
Transfer_STAT	X	1	X	X	1	1	1	0	1	0	1	1	1	"00"		0	1	0	0	0
Transfer_STAT	X	0	X	X	1	1	1	0	1	0	1	1	1	"00"		1	1	0	0	0
EndTransfer_STAT	X	X	1	X	1	1	1	1	1	1	1	1	1	"00"		0	0	0	0	0
EndTransfer_STAT	X	X	0		1	1	1	1	1	1	1	1	1	"00"		0	1	0	0	0
RD_JTAGINSDATA_STAT	X	X	X	1	1	0	0	1	1	1	0	1	1	"00"		0	0	1	1	1
RD_JTAGINSDATA_STAT	X	X	X	1	1	0	0	1	1	1	1	0	1	"00"		0	0	1	1	1
RD_JTAGINSDATA_STAT	X	X	X	1	1	0	0	1	1	1	1	0	1	"00"		0	0	1	0	1
RD_JTAGINSDATA_STAT	X	X	X	0	1	0	0	1	1	1	1	0	1	"00"		0	1	1	1	1
RD_JTAGINSDATA_STAT	X	X	X	0	1	0	0	1	1	1	1	0	1	"00"		0	1	1	1	1
CMET_STAT	X	X	X	X	1	0	0	1	1	1	1	0	1	"00"		0	0	1	0	1
RD_NXTAdrsCMet_STAT	X	X	X	X	1	0	0	1	1	1	1	0	1	"00"		0	0	1	0	1
RD_2ndNXTAdrsCMet_STAT	X	X	X	X	1	1	1	0	1	0	1	1	1	"00"		1	1	0	0	0
LD_NXTAdrsCMet_STAT	X	X	X	X	1	1	1	1	1	1	1	1	1	"00"		0	0	0	0	0

FIG. 11

